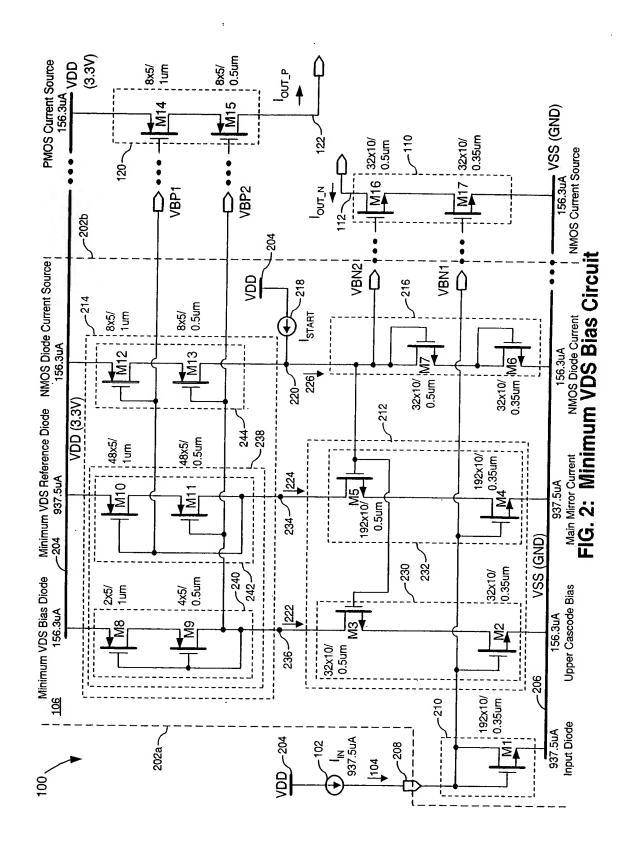


FIG. 1



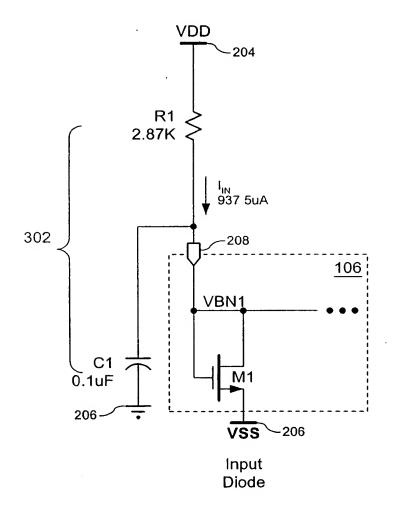
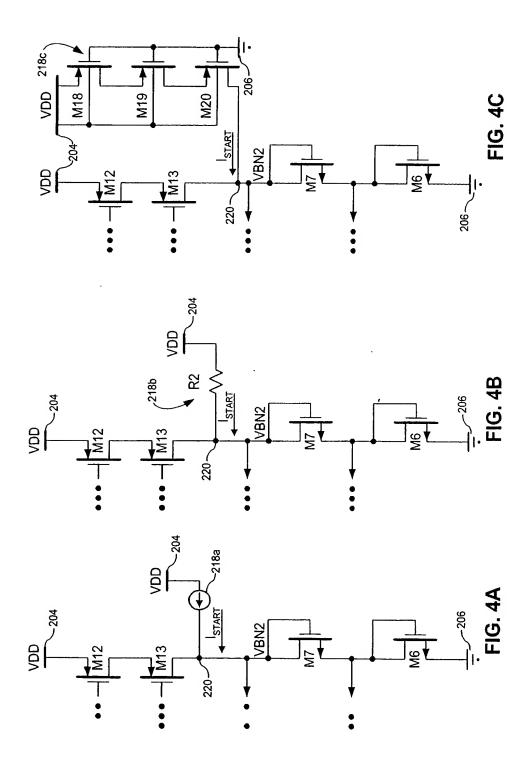
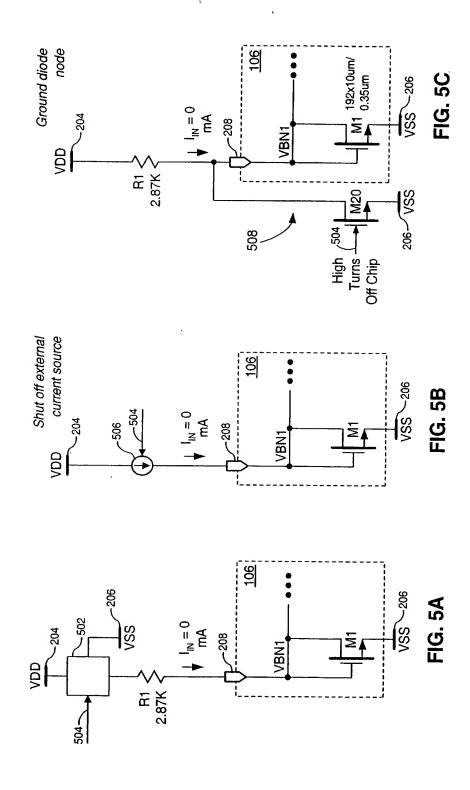


FIG. 3:
Using A Resistor To Supply Input Bias Current





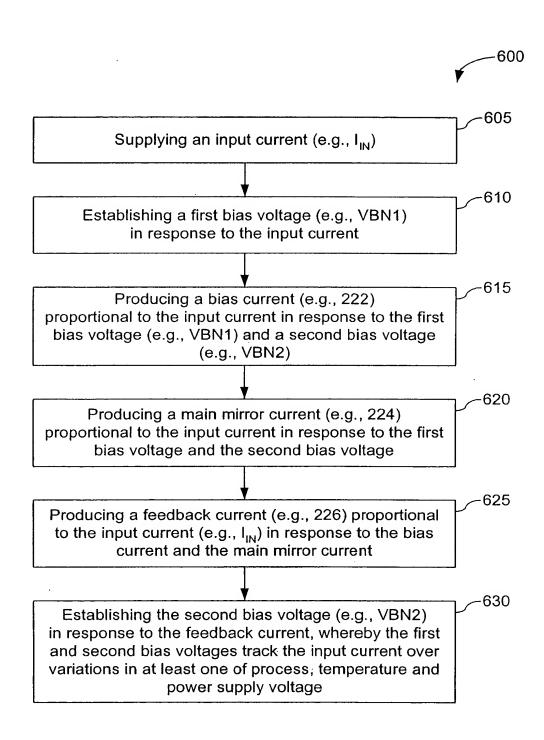


FIG. 6A

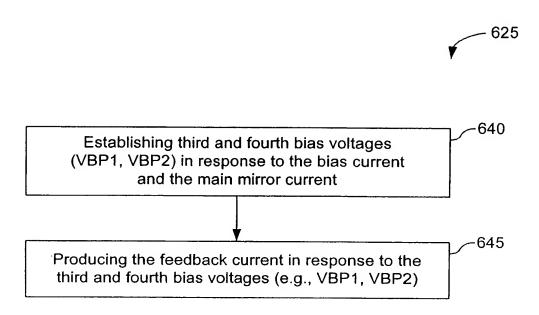


FIG. 6B

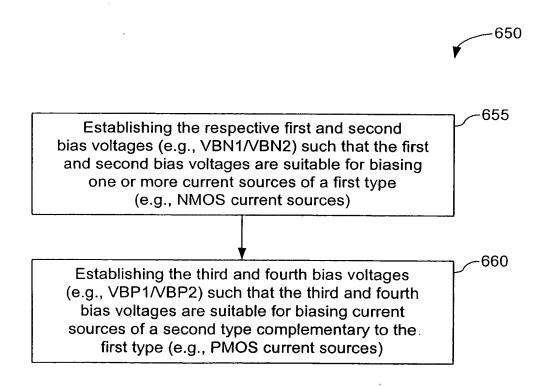


FIG. 6C

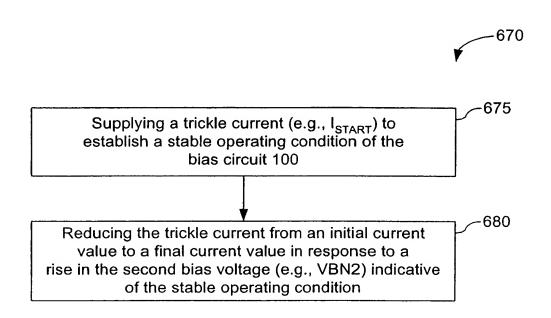
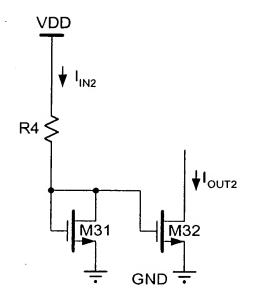


FIG. 6D

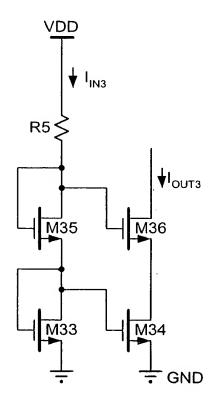


Selectively enabling and disabling the operation of circuit 100 by selectively enabling and disabling the input current (e.g., I_{IN}) in response to an enable/disable signal

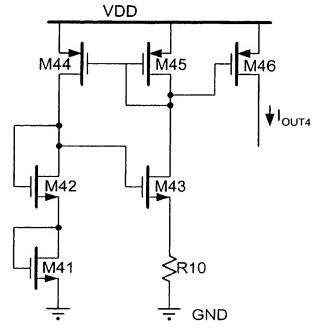
FIG. 6E



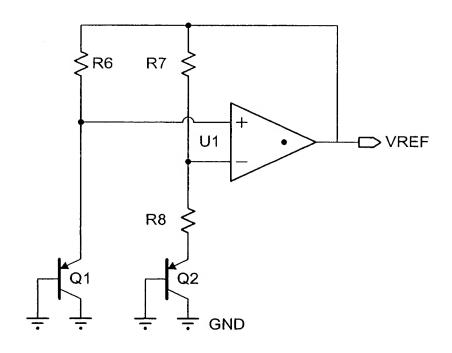
Simple Current Mirror FIG. 7A (Prior Art)



Cascode Current Mirror FIG. 7B (Prior Art)



Self-Biased Current
Mirror With Resistor
FIG. 7C (Prior Art)



Bandgap Voltage Reference FIG. 7D (Prior Art)